

Test Waveform Generator System

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Stefano Rapisarda, Gustavo Cancelo, Neal Wilcer

Hardware status:

-) Working on the implementation of a two-channels test system
 - a) Xilinx X2S150 FPGA Test Card (Digital)
 - b) AD9765 Test Card (Digital to Analog Conversion)
 - c) AD8138 Card (Amplification/Filtering)

Software and Firmware status:

-) Interface for the Xilinx X2S150 FPGA Test Card (Digital)
 - a) Implemented a serial (RS232) interface.
 - b) Implemented a user-friendly GUI based on Microsoft Excel to control and change the waveform generated by the two-channel test system.

TWG Documentation:

http://www-ese.fnal.gov/D0Cal_TWG/

